ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

STRAINED SILICON NMOS DEVICES WITH EMBEDDED SOURCE/DRAIN

Application Number:

Confirmation Number:

First Named Applicant:

Dureseti Chidambarrao

Attorney Docket Number:

FIS920040012

Art Unit:

Examiner:

Search string:

(6063675 or 6261911 or 6406951 or 6440807 or 6607948 or 6303450 or 6326664

or 6346732 or 6403434 or 6489206 or 6620664 or 6657223 or 20030146428 or

20030162348 or 20030219971 or 20030227029).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

Ti	nit	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<u>ج</u> ا۔	12	1	6063675	2000-05-16	Rodder			
	abla	2	6261911	2001-07-17	Lee et al.			
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Examiner Name	Date
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